

This Page Is Inserted by IFW Operations
and is not a part of the Official Record

BEST AVAILABLE IMAGES

Defective images within this document are accurate representations of the original documents submitted by the applicant.

Defects in the images may include (but are not limited to):

- BLACK BORDERS
- TEXT CUT OFF AT TOP, BOTTOM OR SIDES
- FADED TEXT
- ILLEGIBLE TEXT
- SKEWED/SLANTED IMAGES
- COLORED PHOTOS
- BLACK OR VERY BLACK AND WHITE DARK PHOTOS
- GRAY SCALE DOCUMENTS

IMAGES ARE BEST AVAILABLE COPY.

**As rescanning documents *will not* correct images,
please do not report the images to the
Image Problem Mailbox.**

1 / 19

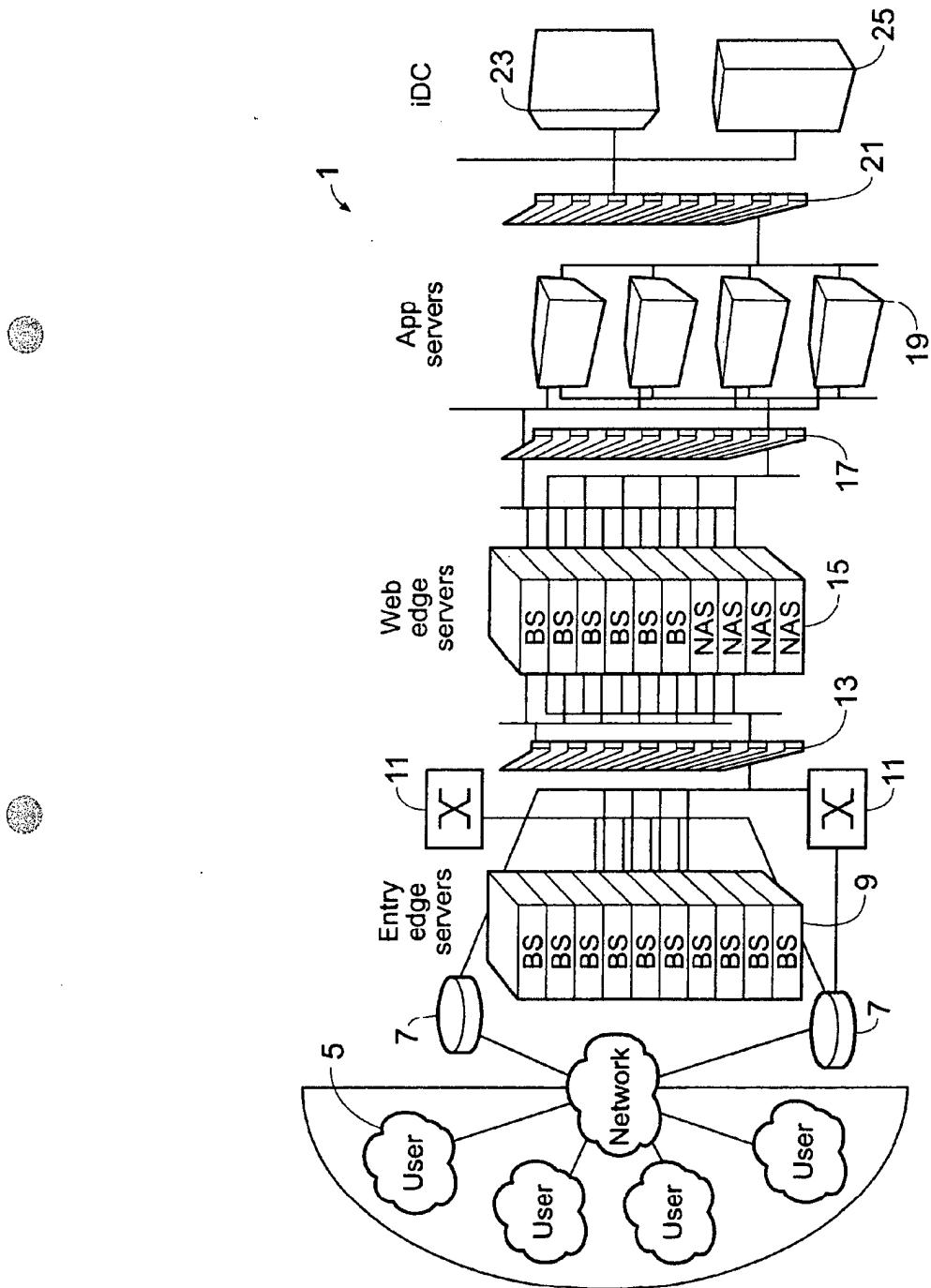


Fig. 1

2119

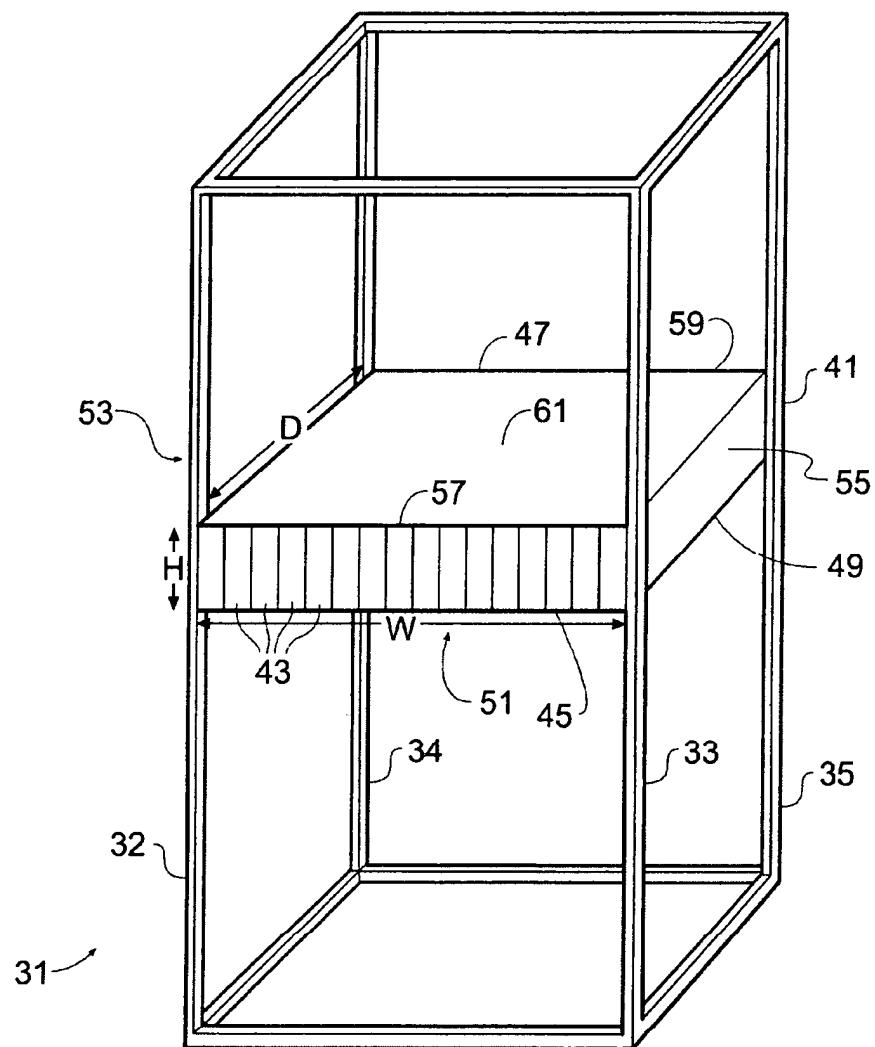


Fig. 2

3/19

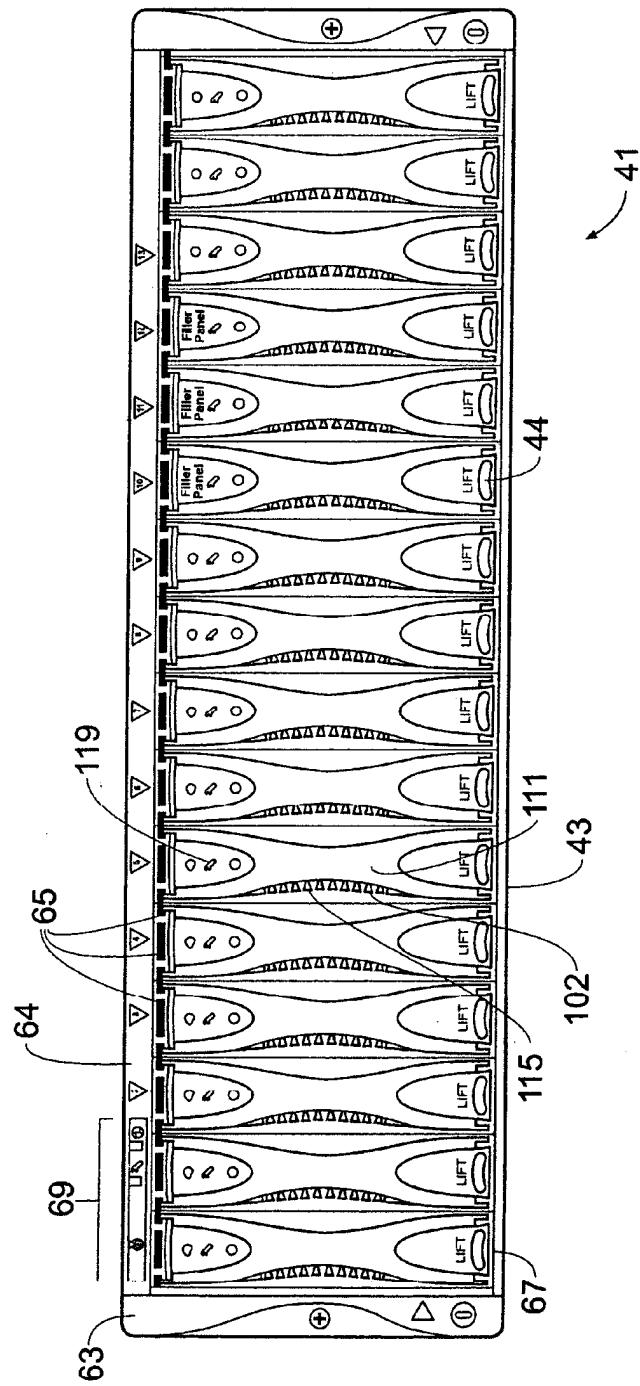


Fig. 3

4/19

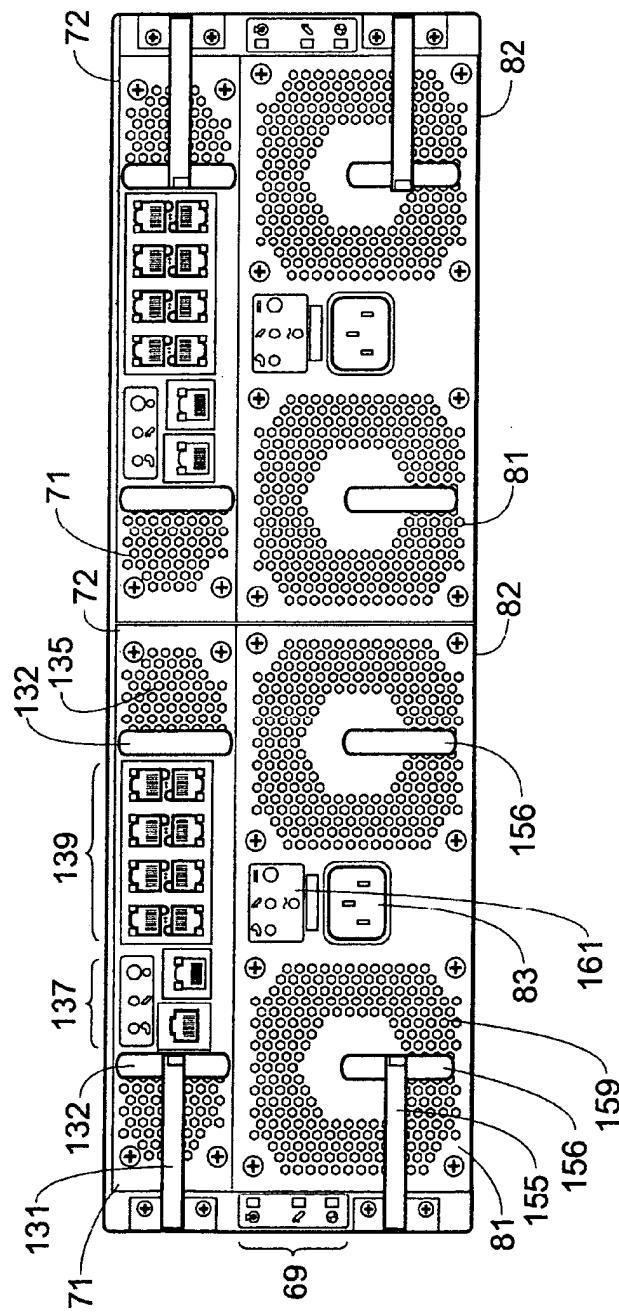
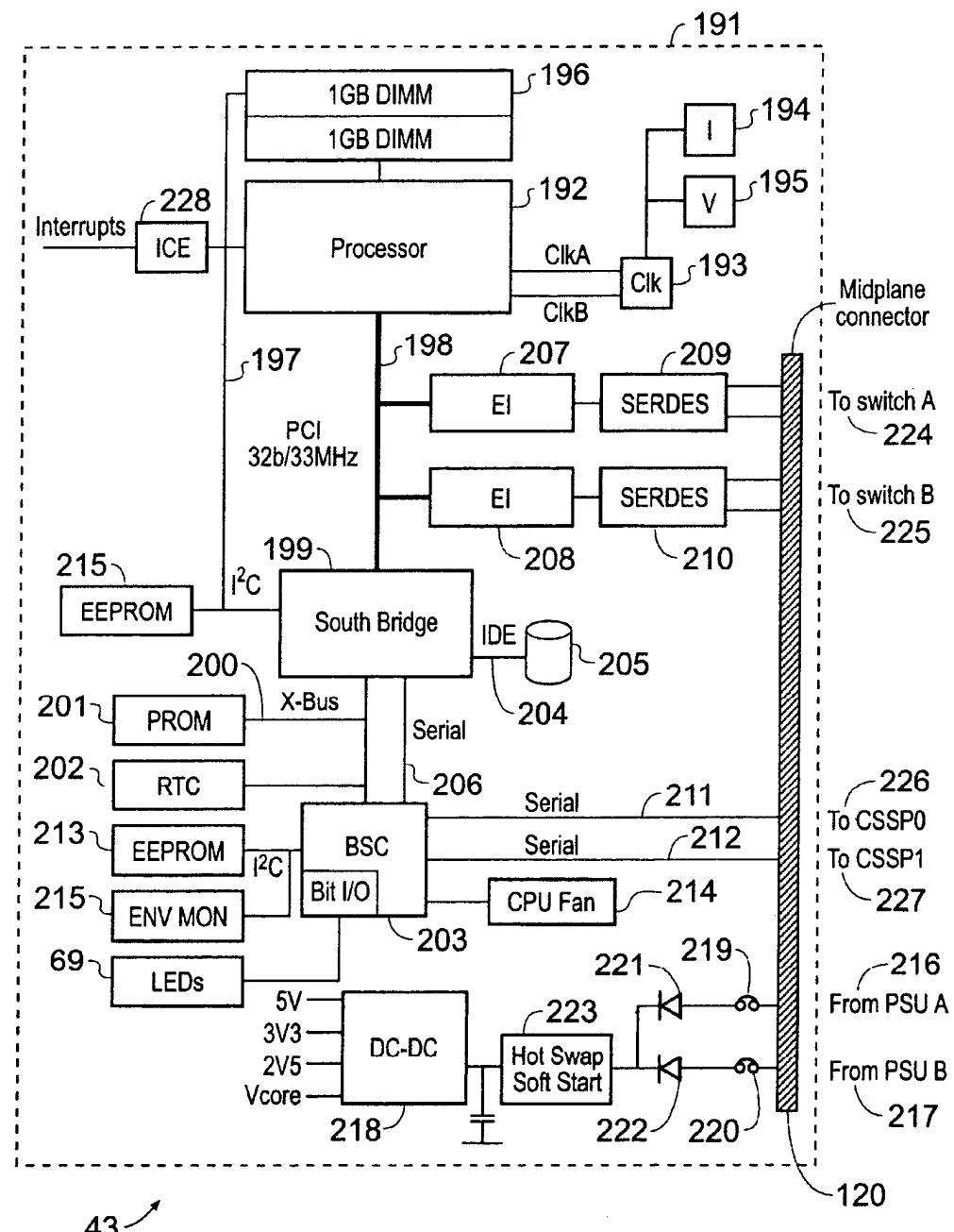


Fig. 4

5/19



43 ↗

Fig. 5

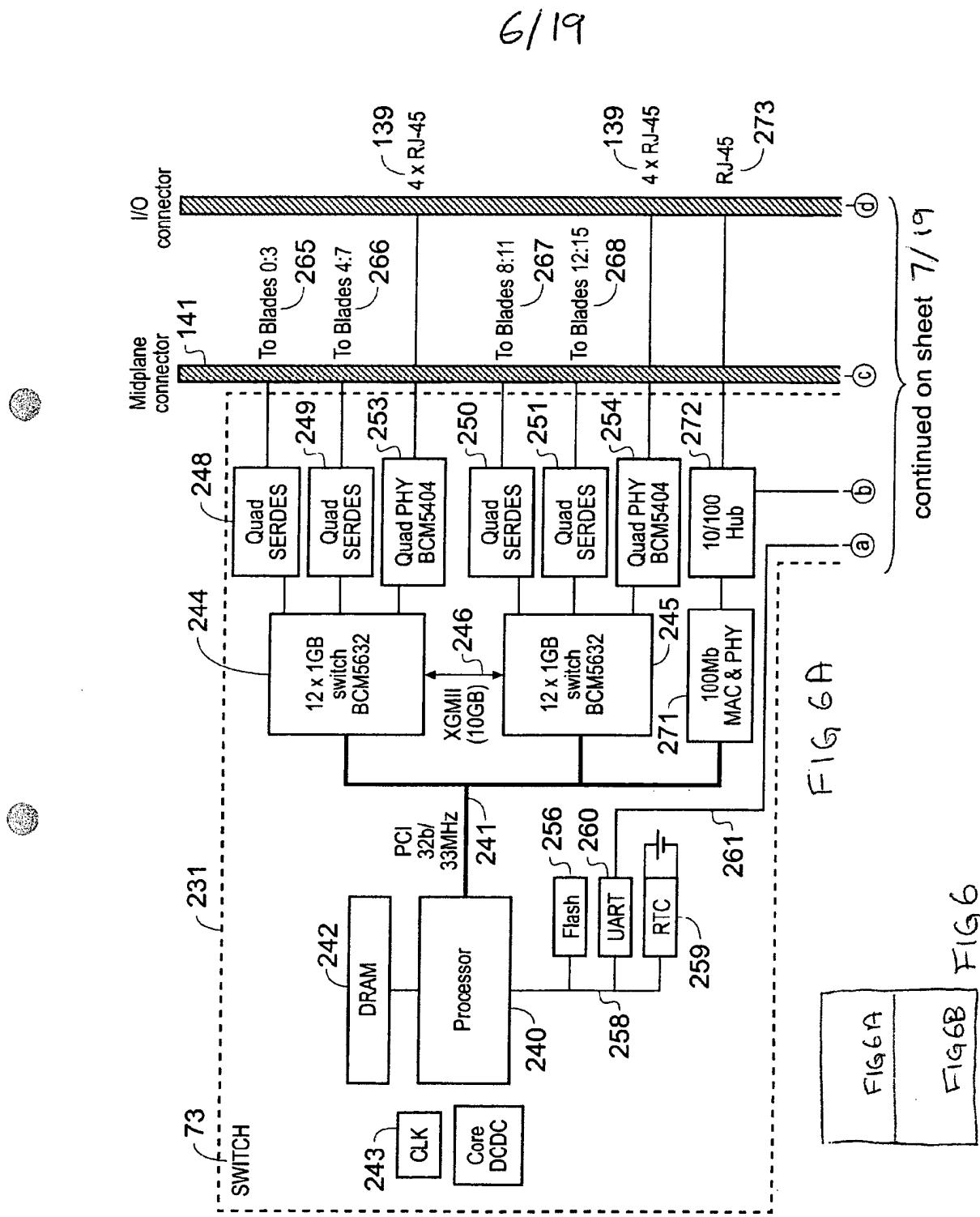
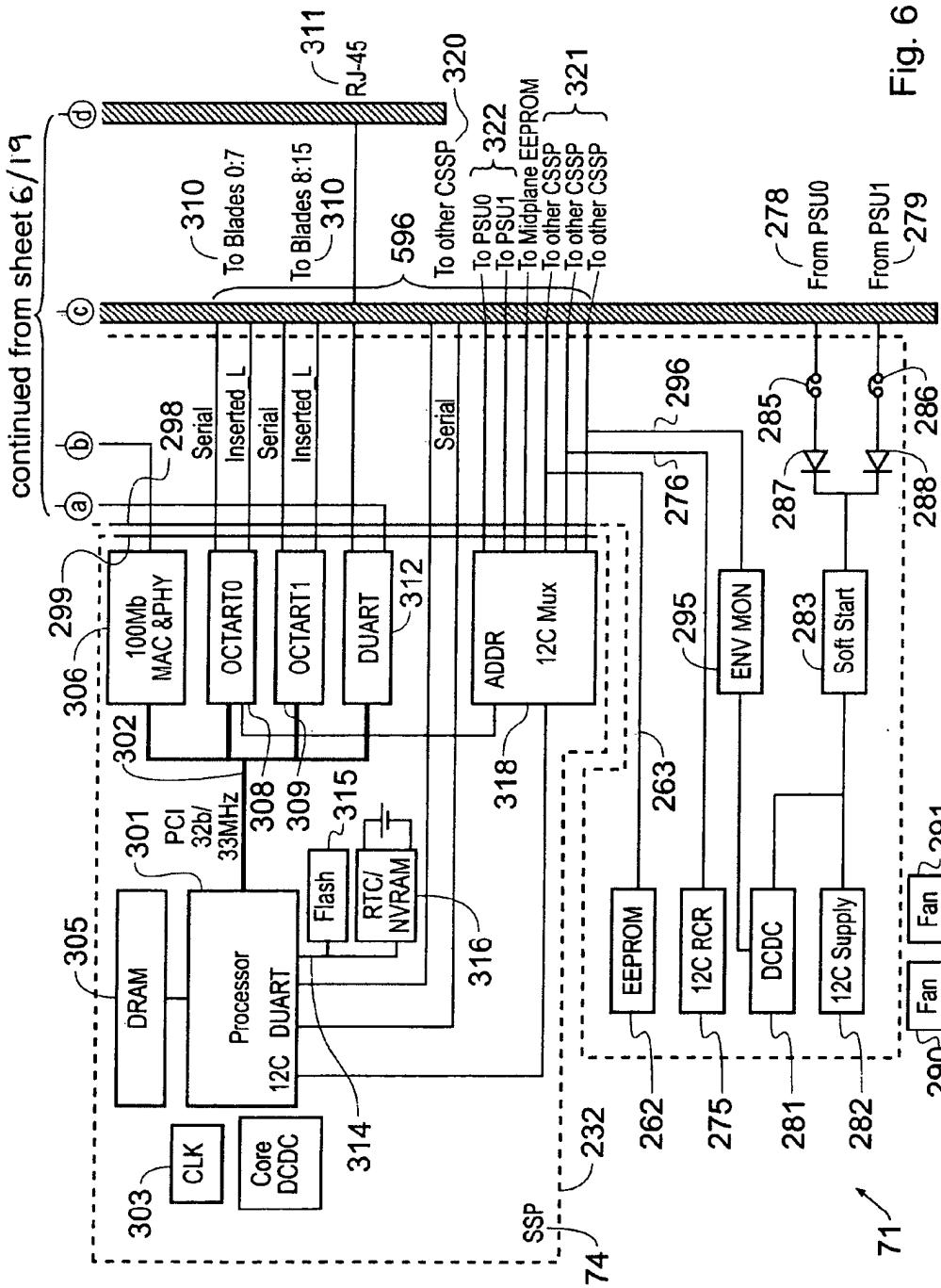


Fig. 6B

71 ↗ 290 Fan Fan ↗ 291
282 12C Supply ↗ 286 288 ↗ 289 From PSU1
281 DCDC ↗ 283 Soft Start ↗ 285 296 ↗ 297 295 263 ↗ 232 262 EEPROM
275 12C RCR ENV MON ↗ 295 296 297 287 283 Soft Start ↗ 285 278 From PSU0
74 SSP 314 Flash RTC/NVRAM 315 312 DUART ADDR 12C Mux 318 316 309 OCTART1 308 OCTART0 301 PCI 32b/33MHz 302 100Mb MAC & PHY 298 Serial Inserted L Serial Inserted L To Blades 8:15 310 RJ-45 To Blades 0:7 310 596 To other CSSP 320 To PSU0 } 322 To PSU1 } To Midplane EEPROM To other CSSP To other CSSP To other CSSP } 321



7/19

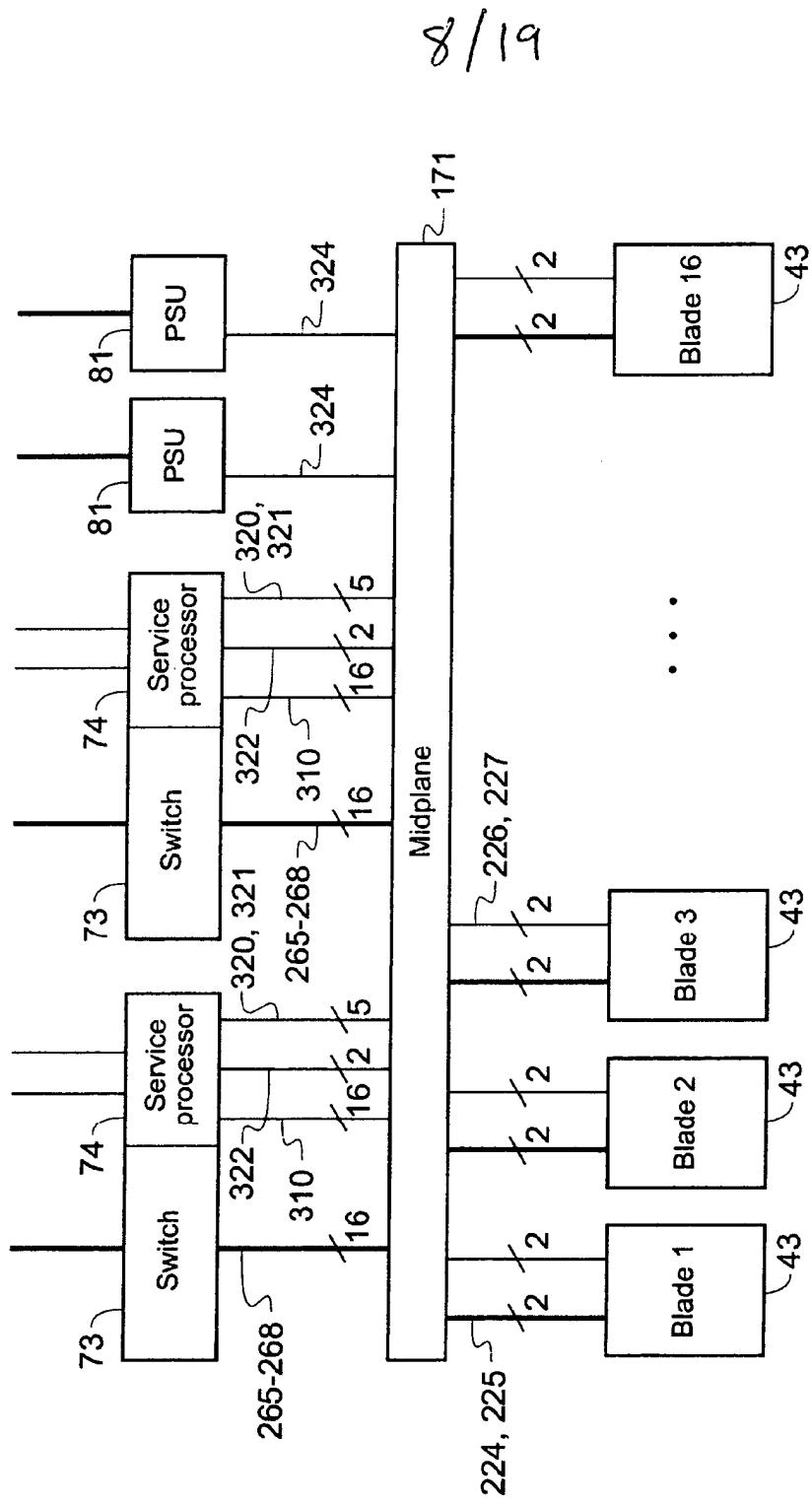


Fig. 7

9/19

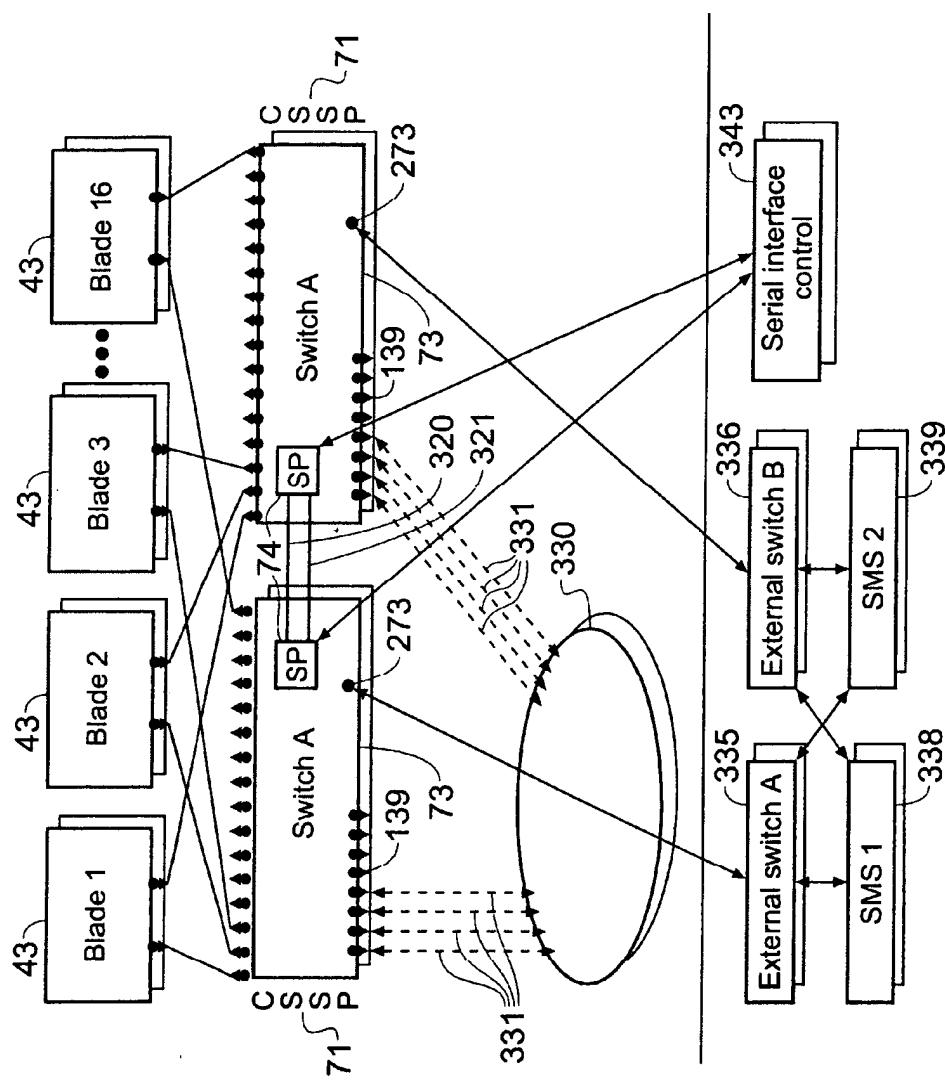


Fig. 8

10/19

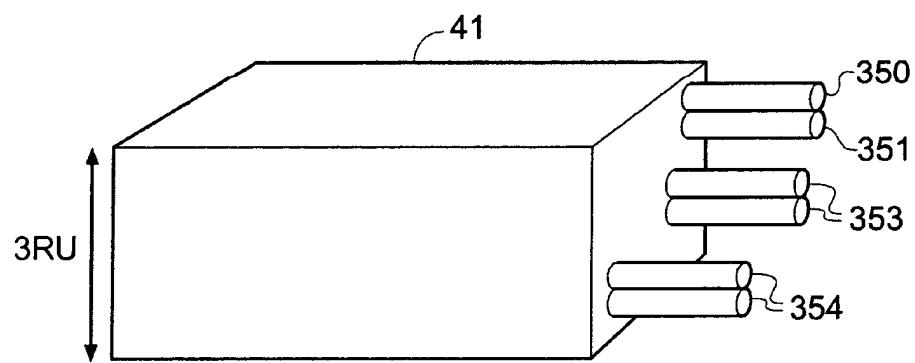


Fig. 9

11/19

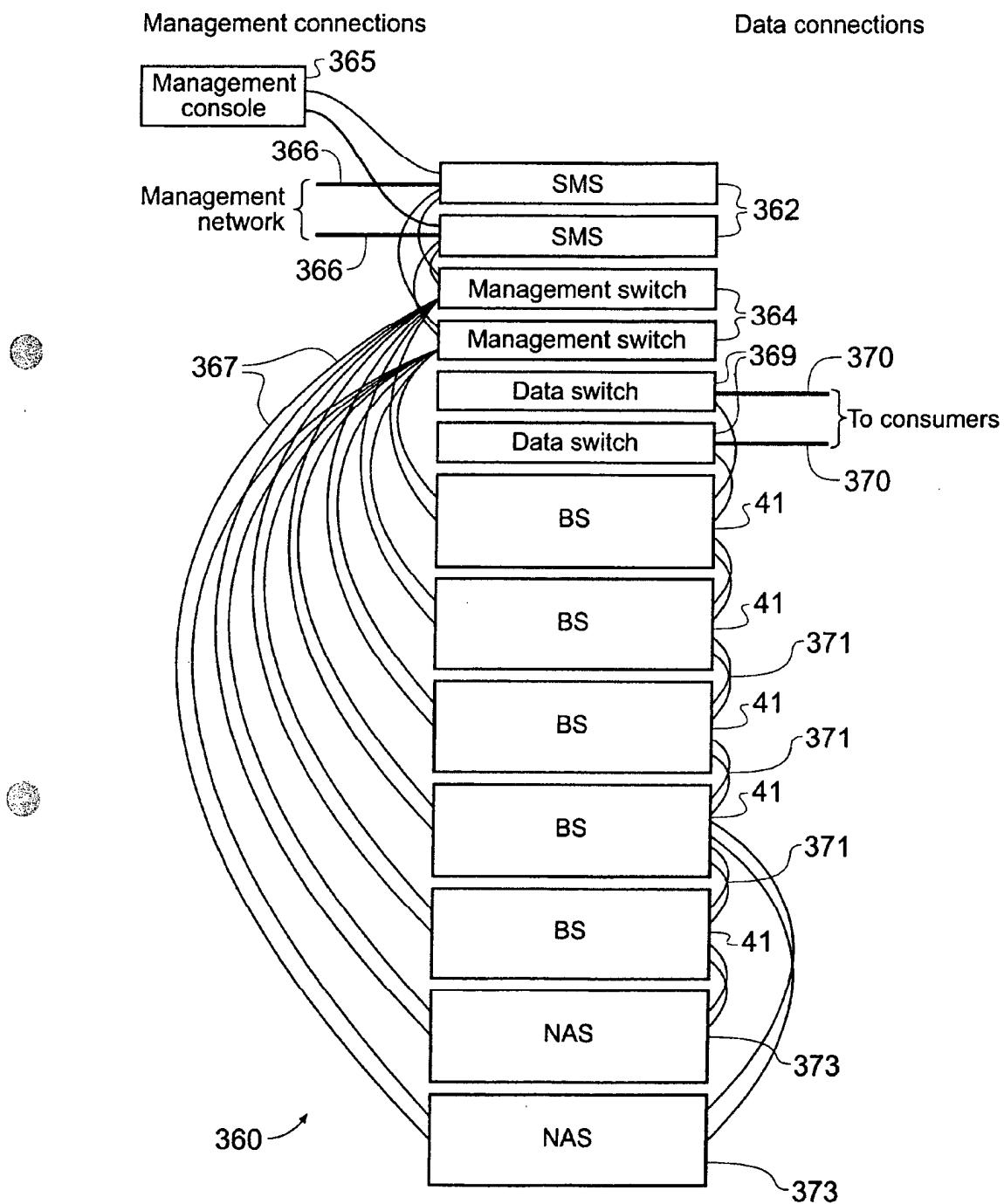


Fig. 10

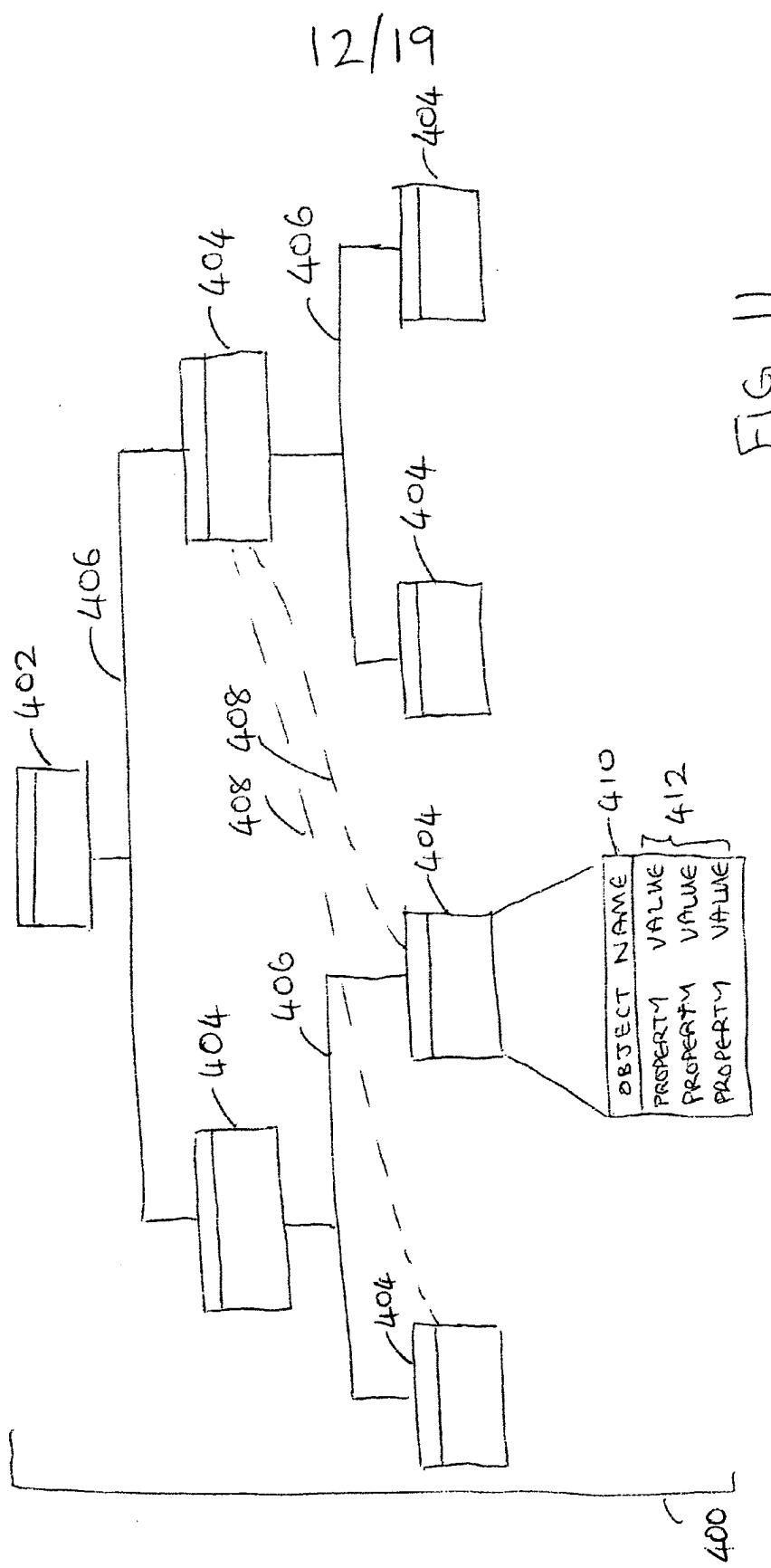


FIG 11

13/19

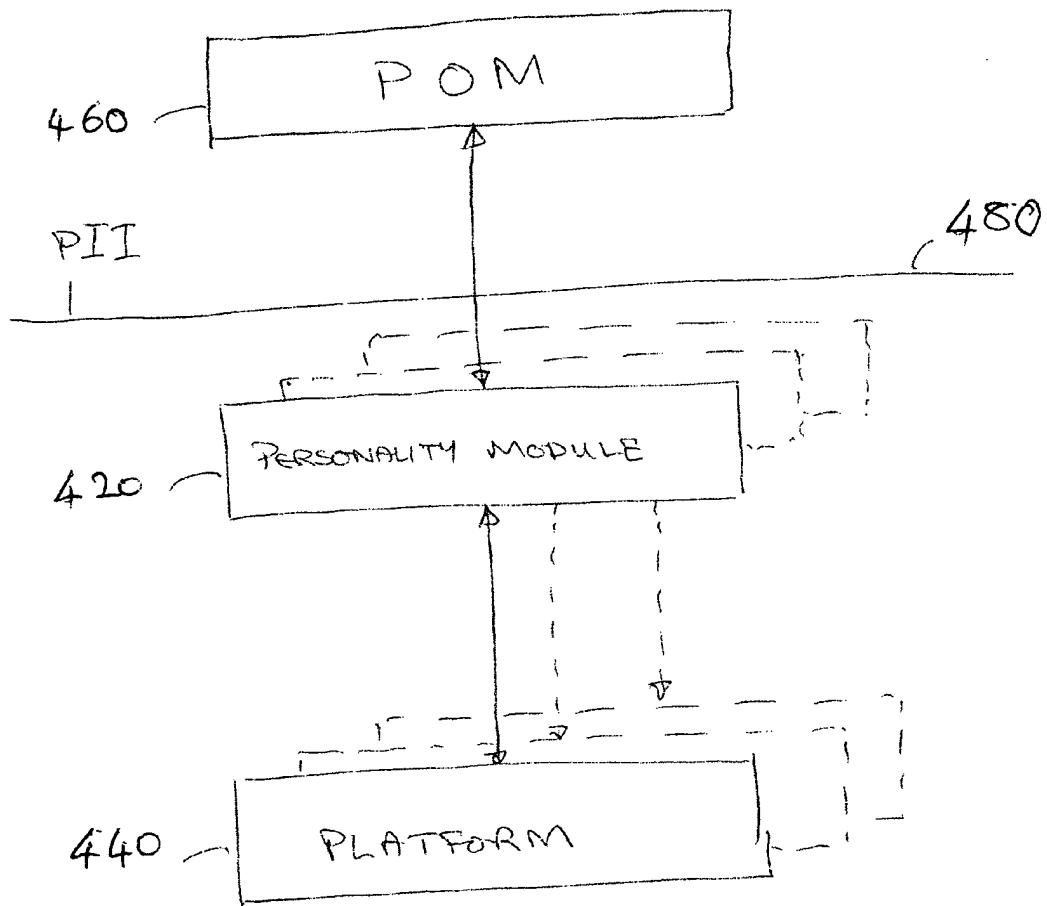


FIG 12

14/19

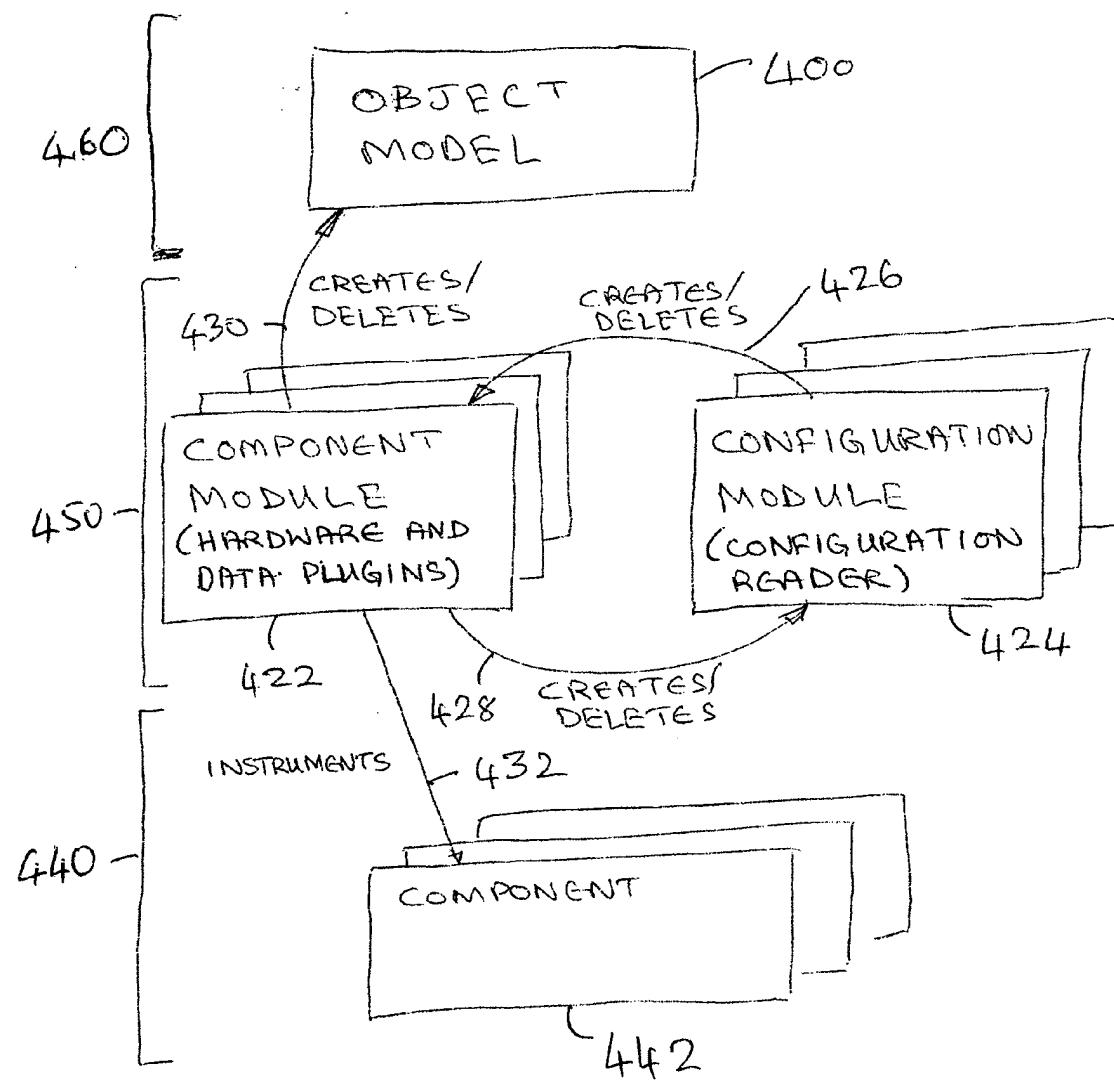


FIG 13

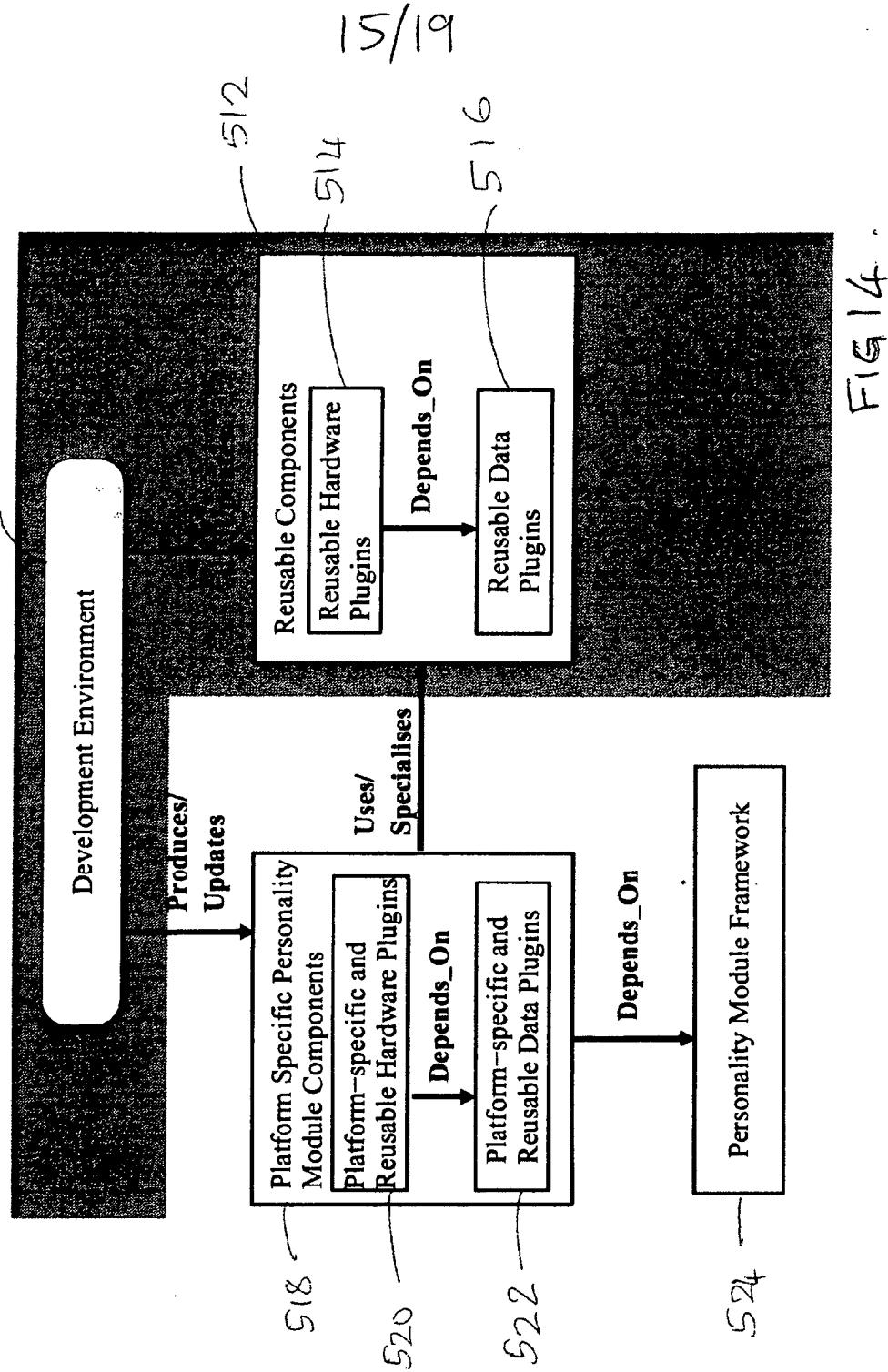


FIG 14.

16/19

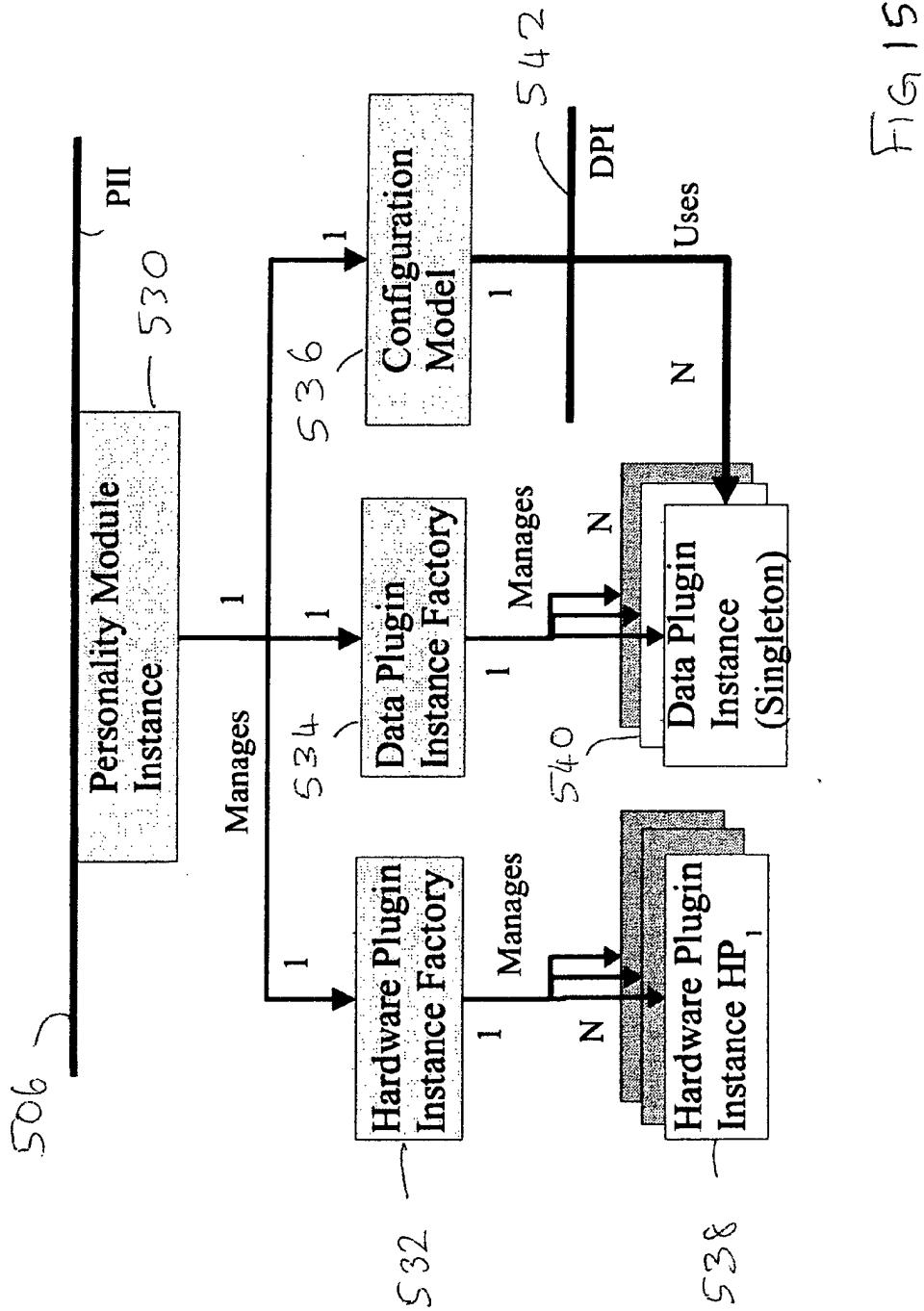
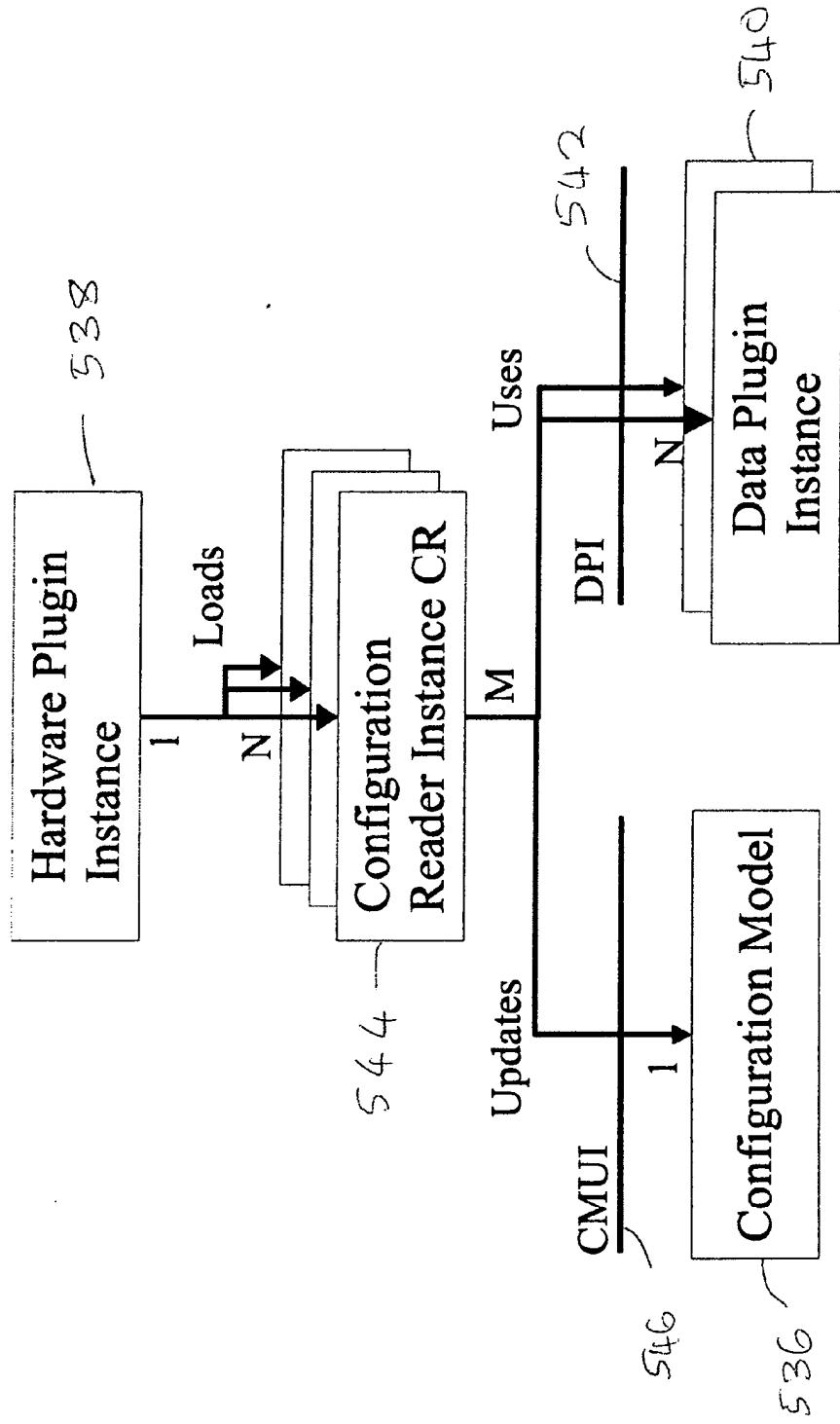


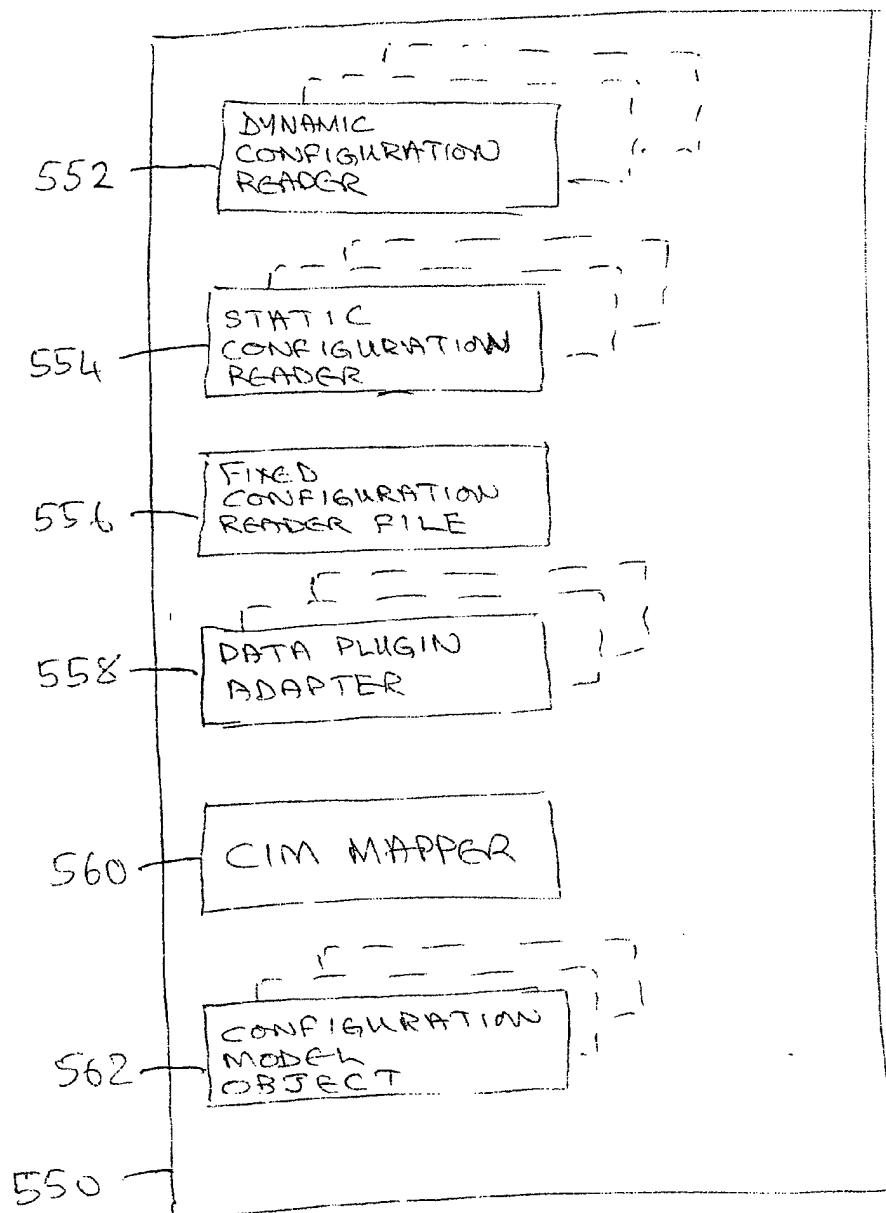
FIG 15

17/19



F16

18/19



FIG, 17

19/19

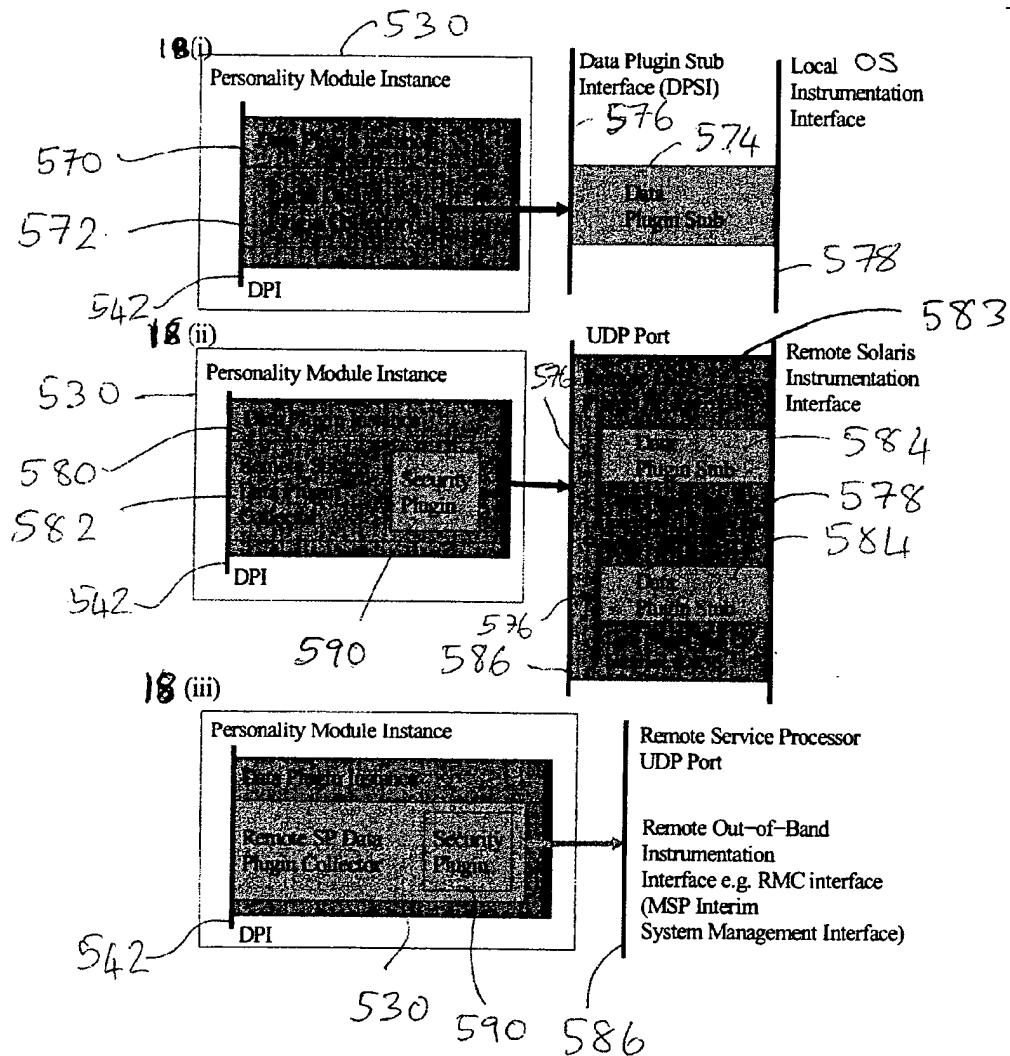


FIG 18